

Listing and Amendments to the Claims

This listing of claims will replace all prior versions and listings of claims in the application:

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Listing of the Claims

1. (currently amended) A switch for applying operating power from a peripheral device power source to a peripheral device, the peripheral device being configured for communication with at least one other electronic device by a data having a bus, the switch including a circuit for sensing communication on said data bus and providing an indication of sensed communication to said peripheral device power source to apply power to said peripheral device in response to said sensed communication, said communication sensing circuit controlled switching arrangement for operating a power supply, the device  
10 comprising:

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~~a bus interface adapted for communicating with a remote device via a bus; and~~

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~~a switch circuit connected between the bus interface and a power supply, the switch circuit operative, when the power supply is in an inactive state, for sensing bus activity and providing a voltage signal for activating the power supply in response to said sensed bus activity~~ a transformer having a first winding coupled to said data bus and a second winding coupled to a first switching transistor, and wherein the switch circuit has no power dissipation when no activity is present on the data bus.

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2. (currently amended) The device switch of claim 1, wherein the ~~switch circuit comprises a first switching transistor having an input coupled to said bus interface, wherein the first switching transistor is brought from a non-conducting state to a conducting state in the presence of pulse signals at said input activity on said data bus.~~

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3. (currently amended) The device switch of claim 2, ~~wherein the switch circuit~~ further comprises ing a second switching transistor having an input coupled to a capacitor, wherein the second switching transistor is brought from a non-conducting state to a conducting state in response to a charge on said capacitor exceeding a threshold level when said first switching transistor is in said conducting state.

4. (currently amended) The device switch of claim 3, ~~wherein the switch circuit~~ further comprises ing a capacitor coupled to the output of the second switching transistor for providing an input voltage to a control circuit of the power supply for activating or inactivating the power supply according to the level of the input voltage.

5. (currently amended) The device switch of claim 1, wherein the power supply further includes a latching circuit responsive to initial activation of said power supply for providing a voltage signal to the power supply sufficient to maintain the power supply in an active state independent of the bus activity.

6. (currently amended) The device switch of claim 5, ~~further comprising wherein~~ a control input ~~coupled to of~~ said power supply is coupled to a controller of said peripheral device for receiving a control signal to cause said power supply to become inactive when there is no activity on said bus.

7. (currently amended) The device switch of claim 1, further comprising means for bypassing said switch ~~circuit~~ to provide a path from a source of input supply voltage to said power supply to cause activation of the power supply independent of bus activity.

CUSTOMER NO.: 24498  
Ser. No. 10/531,220  
Office Action dated: 07/18/06  
Response dated: 08/02/07

PATENT  
PU020449

8. (currently amended) The device of claim 4 6, wherein said  
controller is configured to provide said control signal to said control input of the  
power supply ~~is inactivated~~ after a given time delay based on an absence of  
5 bus activity on said data bus.

Claims 9-17 (cancelled)